

In the claims:

Please amend claims 1, 8, 9 and 13 as follows:

Subcl
1. (TWICE AMENDED) An apparatus for converting signals of a first preselected voltage level to a second preselected voltage level, comprising:

a transistor having an enable terminal, an input terminal, and an output terminal, said input terminal for receiving said signals of the first preselected voltage level, and said output terminal for delivering said signals of the second preselected voltage level;

a capacitor coupled across said input and output terminals of said transistor; and

a resistive element having a first end portion coupled to the enable terminal of said transistor and a second end portion coupled to a voltage supply.

Subcl
8. (TWICE AMENDED) An apparatus for converting signals of a first preselected voltage level to a second preselected voltage level, comprising:

a pass gate transistor having a gate, source, and drain, said drain for receiving said signals of the first preselected voltage level, said source for delivering said signals of the second preselected voltage level, said gate coupled to a voltage supply having a third preselected voltage level;

a capacitor coupled across said source and drain of said pass gate transistor; and

a pump coupled to the gate of said pass gate transistor, said pump being configured to temporarily increase the voltage level applied to said gate.

9. (TWICE AMENDED) An apparatus, as set forth in claim 8, wherein said pump includes a resistive element coupled between the gate of said pass gate transistor and said voltage supply, and a capacitor coupled to the gate of said pass gate transistor [and receiving] to receive said input signal.

Sub 13. (TWICE AMENDED) An apparatus for converting an input signal of a first preselected voltage level to a second preselected voltage level, comprising:

a pass gate transistor having a gate, source, and drain, said drain for receiving said signals of the first preselected voltage level, said source for delivering said signals of the second preselected voltage level, said gate being coupled to a voltage supply having a third preselected voltage level;

a capacitor coupled across said source and drain of said pass gate transistor; and

means for temporarily increasing the voltage level applied to said gate.